

	Document ID	Issue Date	Pages	Title	Current OR
1	US 20030156603 A1	20030821	149	Apparatus and method for trellis encoding data for transmission in digital data transmission systems	370/485
2	US 20030142764 A1	20030731	84	Single chip VLSI implementation of a digital receiver employing orthogonal frequency division multiplexing	375/341
3	US 20020015423 A1	20020207	148	Apparatus and method for trellis encoding data for transmission in digital data transmission systems	370/485
4	US 20010046266 A1	20011129	149	Apparatus and method for scdma digital data transmission using orthogonal codes and head end modem with no tracking loops	375/259
5	US 20010024474 A1	20010927	149	Apparatus and method for trellis encoding data for transmission in digital data transmission systems	375/259
6	US 20010001616 A1	20010524	149	Apparatus and method for SCDMA digital data transmission using orthogonal codes and a head end modem with no tracking loops	375/259
7	US 6687315 B2	20040203	83	Single chip VLSI implementation of a digital receiver employing orthogonal frequency division multiplexing	375/341
8	US 6665308 B1	20031216	136	Apparatus and method for equalization in distributed digital data transmission systems	370/441
9	US 6621794 B1	20030916	17	Method and apparatus for measuring the timing difference between physical IMA links and for delivering a time difference to the IMA layer	370/235
10	US 6333940 B1	20011225	140	Integrated digital loop carrier system with virtual tributary mapper circuit	370/506
11	US 6307868 B1	20011023	127	Apparatus and method for SCDMA digital data transmission using orthogonal codes and a head end modem with no tracking loops	370/485
12	US 5812497 A	19980922	14	Hybrid-synchronous type clock synchronizing apparatus of which dominant gain greater than sum of other gains network therewith, and clock synchronizing method thereof	368/10

	<b>Current XRef</b>	<b>Inventor</b>
1		Rakib, Selim Shlomo et al.
2	375/354; 714/795	Keevill, Peter A et al.
3	370/487; 370/503; 370/516	Rakib, Selim Shlomo et al.
4	375/354; 375/371	Rakib, Selim Shlomo et al.
5	375/354	Rakib, Selim Shlomo et al.
6	375/344	Rakib, Selim Shlomo et al.
7	375/326	Keevill; Peter A et al.
8	370/442; 370/479; 370/503; 375/222; 375/233	Rakib; Selim Shlomo et al.
9	370/236.2	Heikkinen; Pekka et al.
10	370/466; 370/509	Baydar; Ertugrul et al.
11	370/516; 375/325; 375/326	Rakib; Selim Shlomo et al.
12	368/156; 368/46; 375/356	Yahata; Haruki

	Document ID	Issue Date	Pages	Title	Current OR
13	US 5787133 A	19980728	21	Signal modulation employing a pseudo-random sequence of pilot symbols	375/366
14	US 5666378 A	19970909	20	High performance modem using pilot symbols for equalization and frame synchronization	375/222
15	US 5615177 A	19970325	14	Hybrid-synchronous type clock synchronizing apparatus of which dominant gain greater than sum of other gains, network therewith, and clock synchronizing method thereof	368/10
16	US 5526383 A	19960611	71	Network control system for controlling relative errors between network nodes	375/356
17	US 5524029 A	19960604	70	Network control system for controlling relative errors between network nodes	375/356
18	US 5515401 A	19960507	71	Network control system for controlling relative errors between network nodes	375/344
19	US 5166924 A	19921124	47	Echo cancellation in multi-frequency differentially encoded digital communications	370/289
20	US 4912706 A	19900327	11	Frame synchronization in a network of time multiplexed optical space switches	370/507

	<b>Current XRef</b>	<b>Inventor</b>
<b>13</b>	375/367; 714/775	Marchetto; Robert F. et al.
<b>14</b>	375/231; 375/235; 375/340	Marchetto; Robert F. et al.
<b>15</b>	368/156; 368/46; 375/356	Yahata; Haruki
<b>16</b>	375/358; 455/423; 455/502	Takenaka; Tetsuyoshi et al.
<b>17</b>	370/350; 375/358; 455/502	Takenaka; Tetsuyoshi et al.
<b>18</b>	375/356; 455/502; 455/71	Takenaka; Tetsuyoshi et al.
<b>19</b>	370/291; 379/406.1 3	Moose; Paul H.
<b>20</b>	370/517; 375/358; 398/52; 398/55	Eisenberg; Martin et al.